

ابسم الله الرحمن الرحيم

المقدمة

Capacitors

Capacitor Bank

voltage Regulator

(Voltage Regulator)

(Single phase)

(Three phase)

(Single phase)

. (Three phase)

. Ac Automatic Voltage Regulator

(Technical characteristic)

(Out put capability) -

%

Over load capability -

Over load ratio	Over load time not permitted (min)
%	
%	
%	

-:

-

-

()

-

-

low tension

$$P_{in} = P_{out}$$

P output

$$P_{in} = P_{out}$$

$$(P = VI \cos \phi) \quad (\cos \phi)$$

()

()

$$P = V \cdot I$$

= *

= *

(cos o)

$$P = \dots V I \cos o$$

(Capacitor Bank)

%

(sin o)

$$(\sin o) \quad (Q = \dots V I \sin o)$$

capacitor

$$\dots (Q = \dots V I \sin o)$$

()

()

ph

- :

()

-

()

(starting current)

-

* ()

-

* (-)

-

* ()

-

()

()

()

()

(-)

()

()

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Capacitor bank

(Ac voltage Regulator)

(Ac voltage Regulator)

(Voltage regulator)

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